

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,960,958 B2  
DATED : November 1, 2005  
INVENTOR(S) : Matsuura et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 18,

Lines 13-21, should read:

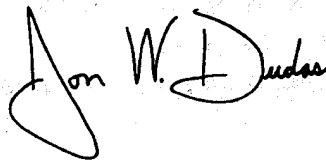
-- 2. The control method for the low-distortion power amplifier according to claim 1, comprising the steps of:

detecting a magnitude of the distortion signal that is output from the distortion extraction circuit; and

controlling at least one of the amplitude frequency characteristic adjustment circuit or circuits and the vector adjustment circuit or circuits so as to minimize the detected magnitude of the distortion signal. --.

Signed and Sealed this

Thirtieth Day of May, 2006

A handwritten signature in black ink, appearing to read "Jon W. Dudas", is written over a faint, rectangular grid background.

JON W. DUDAS

*Director of the United States Patent and Trademark Office*